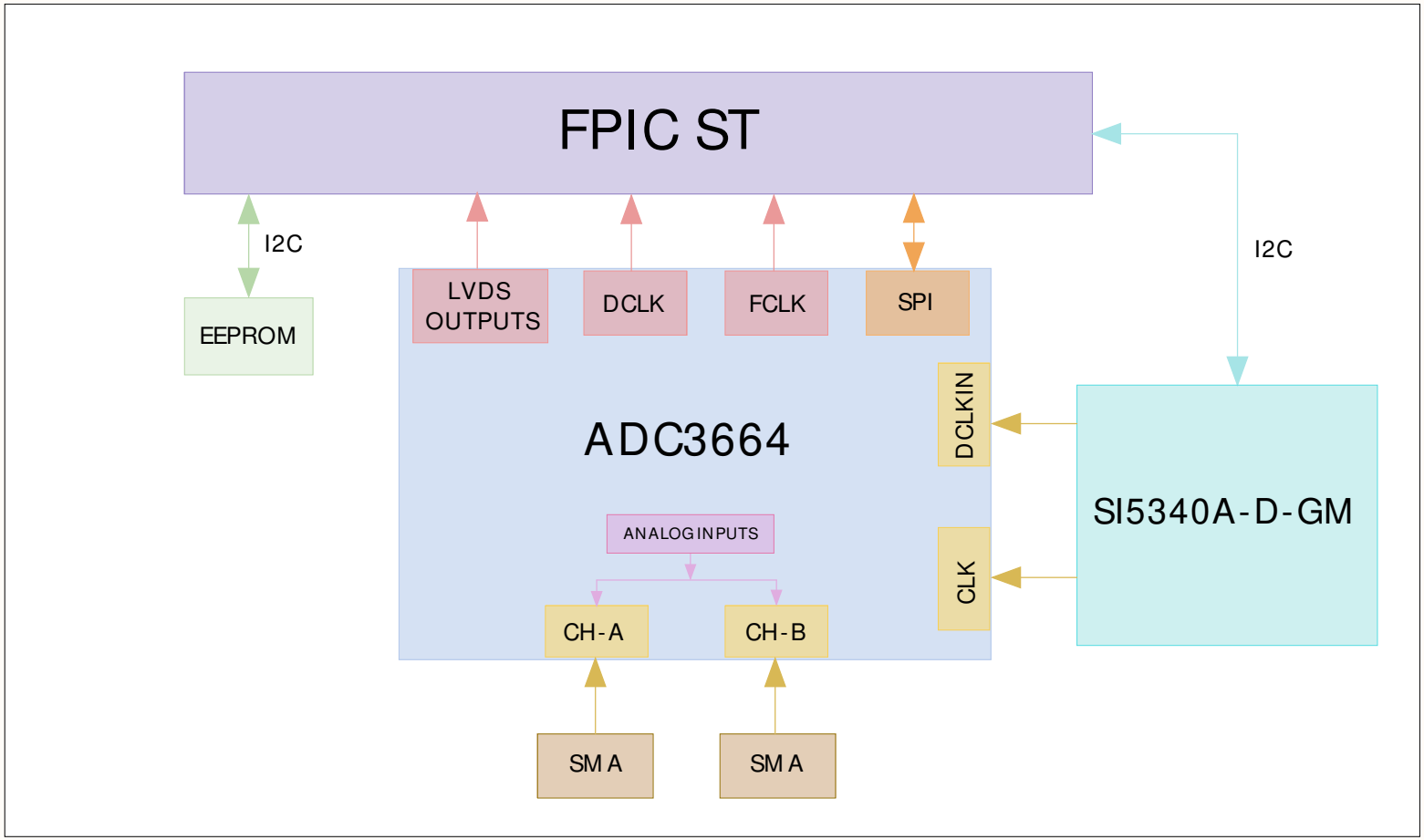
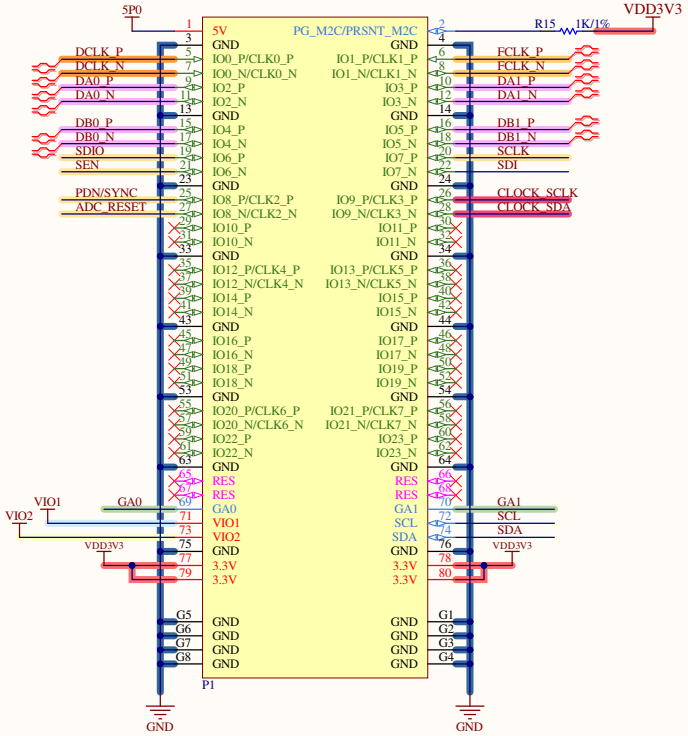
Sheet No	Contents	Sheet Name
1	Block diagram	BLOCK DIAGRAM
2	FPIC HEADER, EEPROM, POW STATUS LED	FPIC ST
3	ADC3664	ADC
4	CLOCK GENERATOR	ADC CLK

BLOCK DIAGRAM

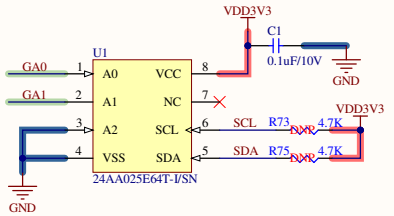


Title: Block Diagram		Revision: A1	Numato Lab
Size: A3	Project: FPICST004.PrjPcb		
Date: 15-09-2026	Time: 09:05:04	Sheet 1 of 4	
File: BLOCK DIAGRAM.SchDoc			

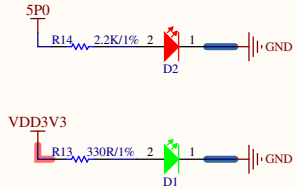
FPIC ST



EEPROM



PWR INDICATION



Title: FPIC GT		Revision: A1	
Size: A3	Project: FPICST004.PrjPcb		
Date: 15-09-2026	Time: 09:05:05	Sheet 2 of 4	
File: FPIC_ST.SchDoc			

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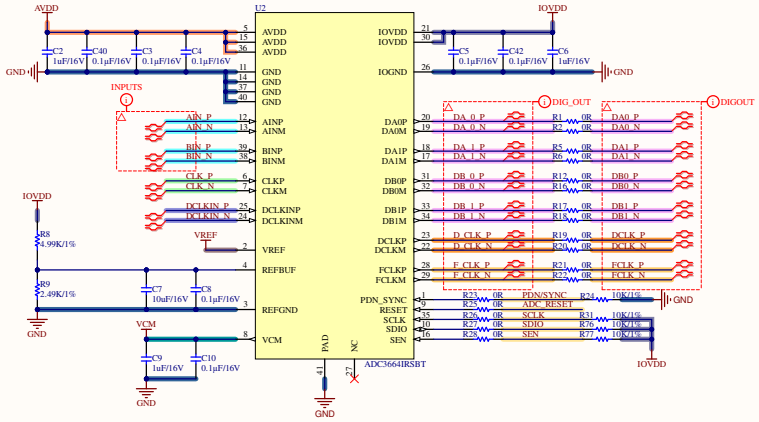
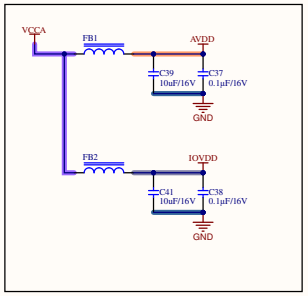
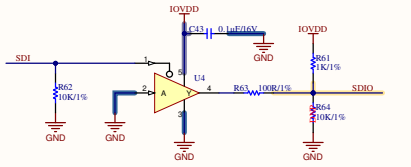
ADC

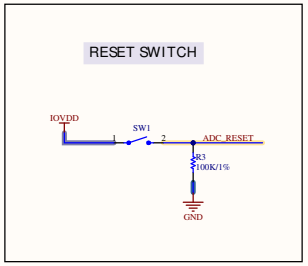




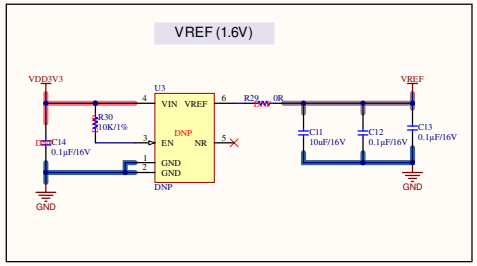
FPGA SDI to ADC SDIO interface



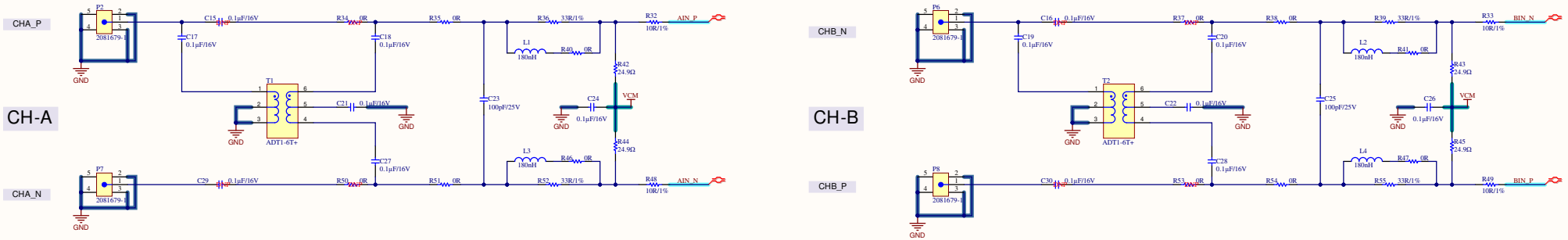
RESET SWITCH



VREF (1.6V)



ANALOG INPUTS



Title: XXXX	Revision: V1.0
Size: A2	Project: FPICSTI004.PrjPcb
Date: 15-09-2026	Time: 09:05:06
Sheet 3 of 4	
File: ADC.SchDoc	

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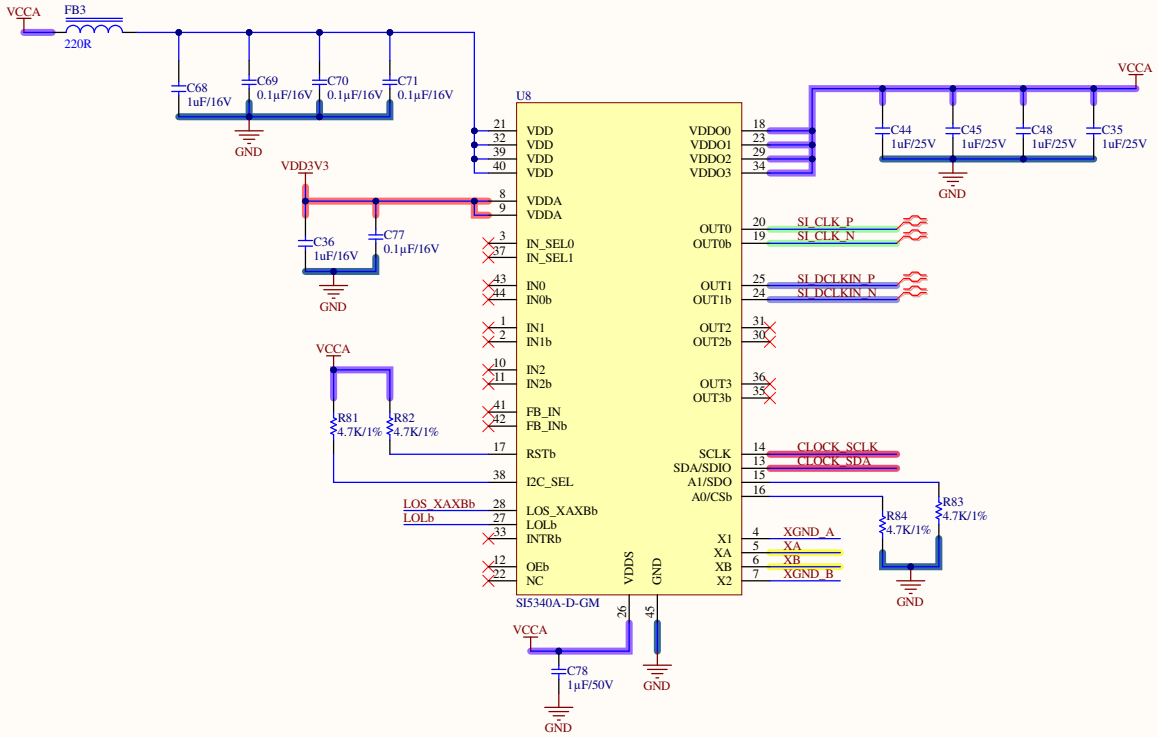
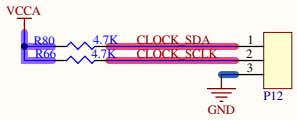
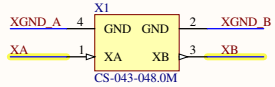
CLOCK GENERATOR



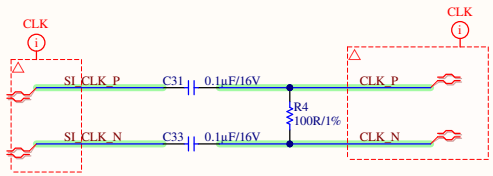
12C HDR



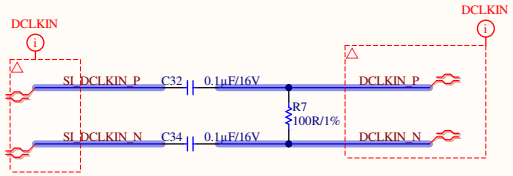
CRYSTAL 48MHz



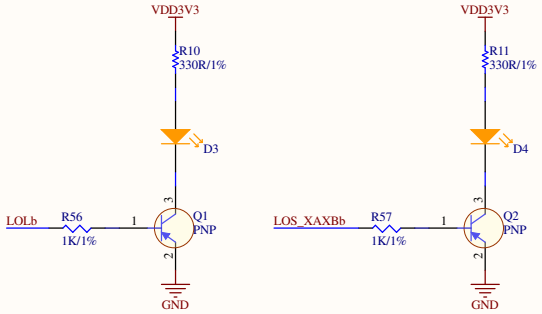
SAMPLING CLK



DCLKIN



Fault Indication



Title: ADC INPUTS		Revision: A1	
Size: A3	Project: FPICST004.PrjPcb		
Date: 15-09-2026	Time: 09:05:07	Sheet 4 of 4	
File: ADC CLK.SchDoc			

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