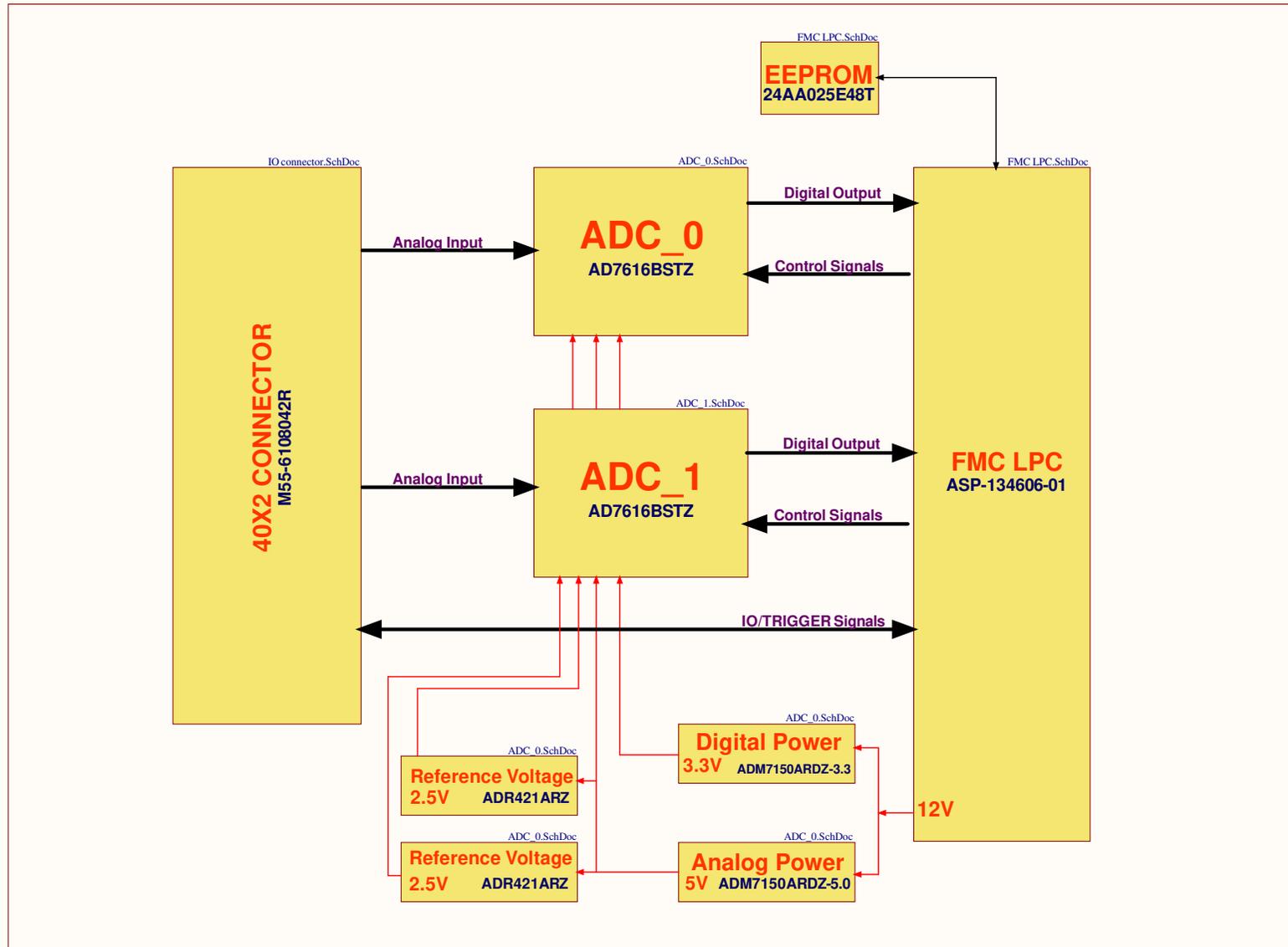


BLOCK DIAGRAM



ANALOG INPUT

ADC_1

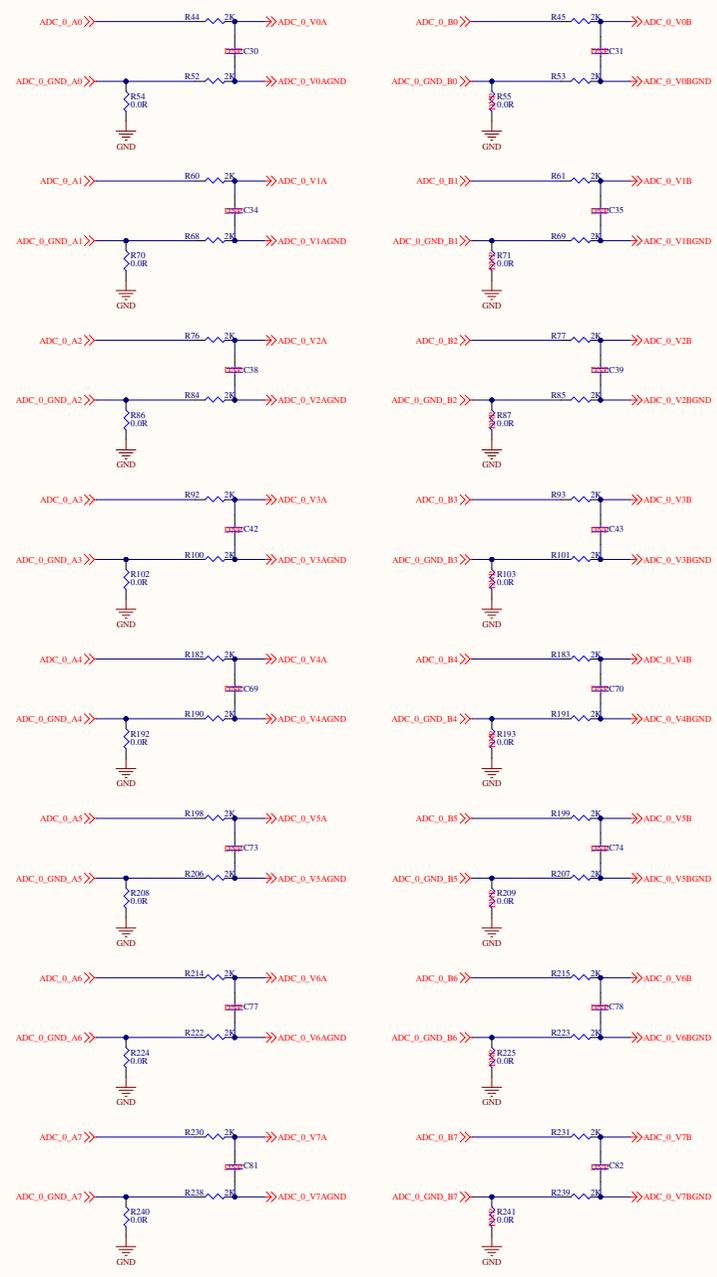
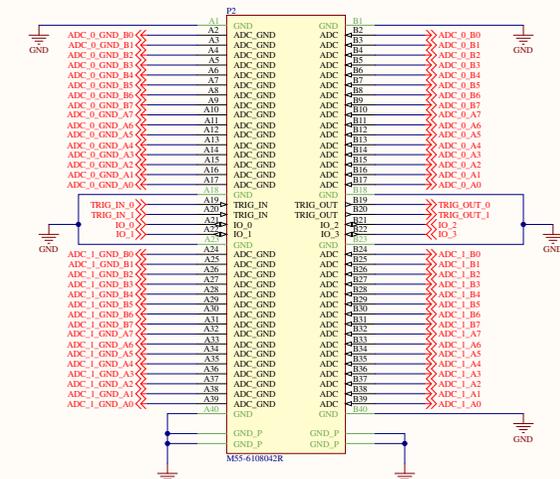
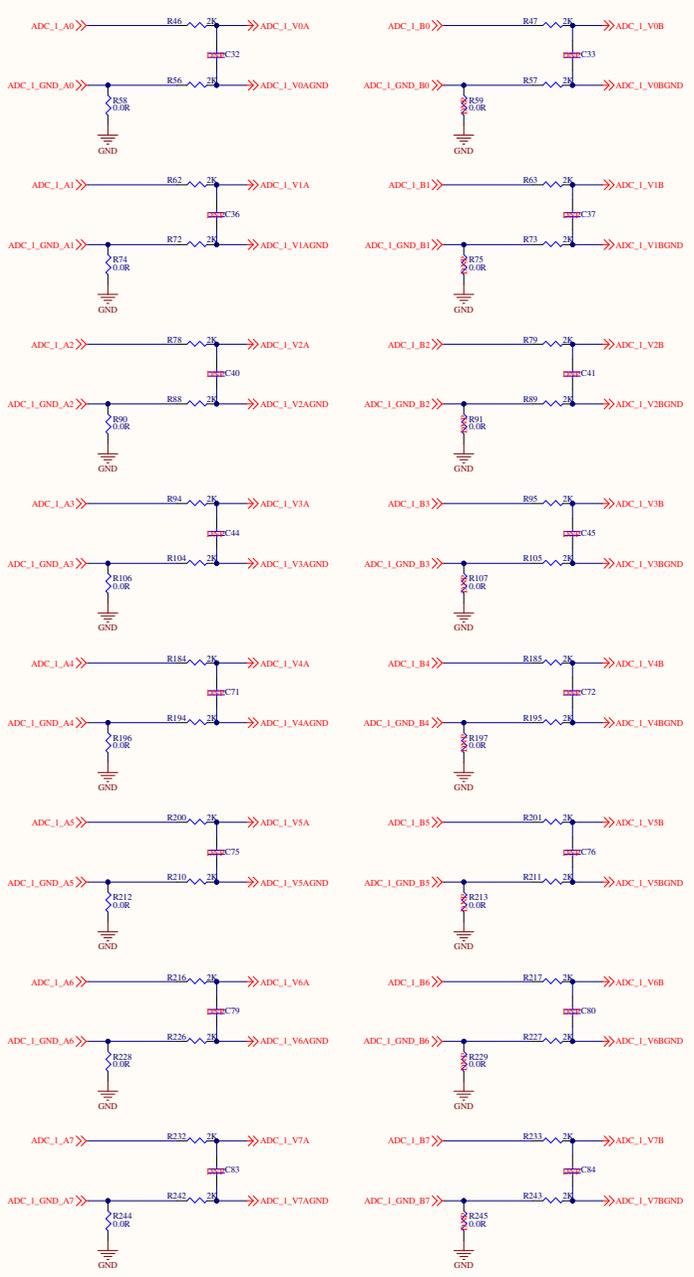
ADC_0

A0-A7

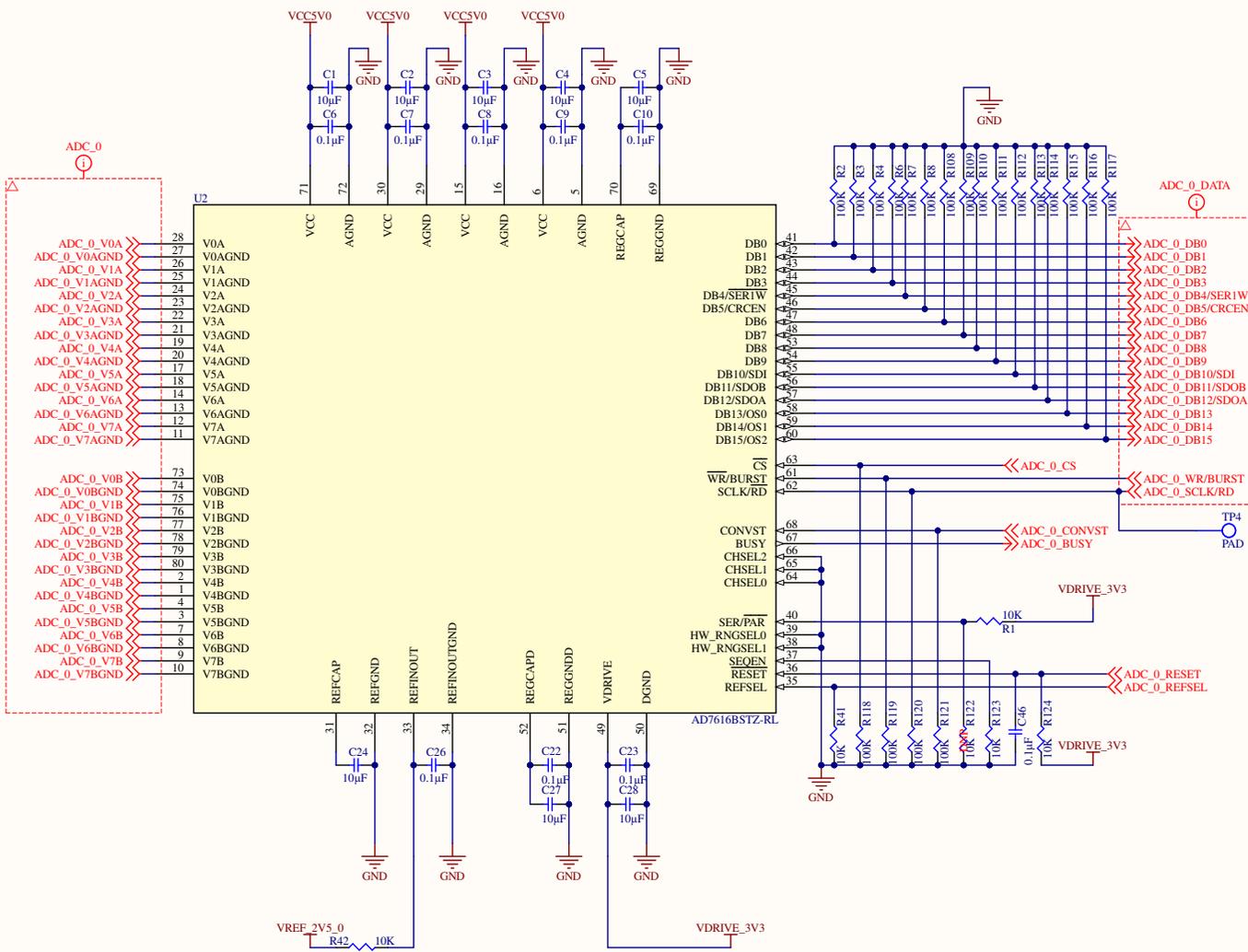
B0-B7

C0-C7

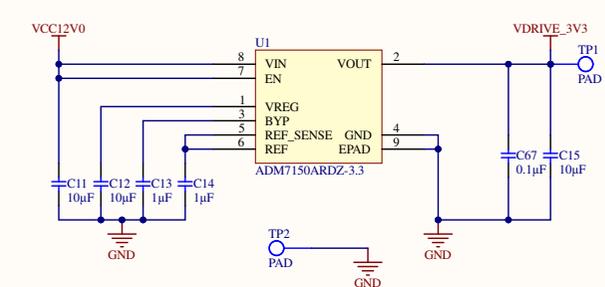
D0-D7



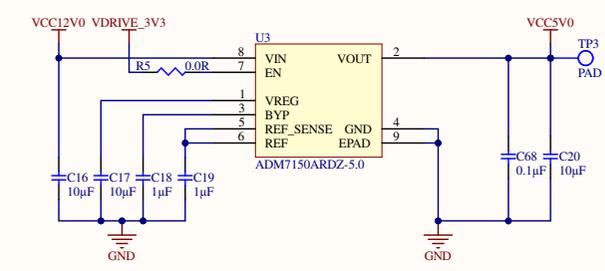
ANALOG TO DIGITAL CONVERTER ADC_0



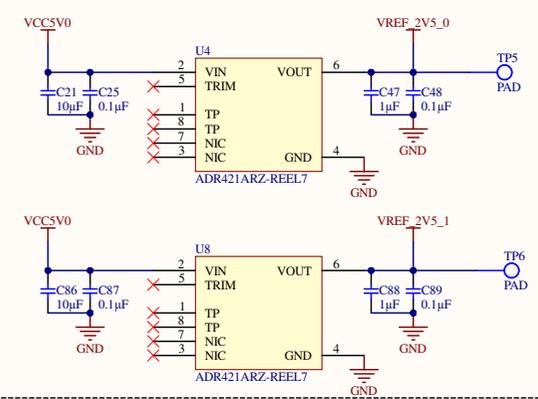
ADC POWER VDRIVE_3V3 POWER



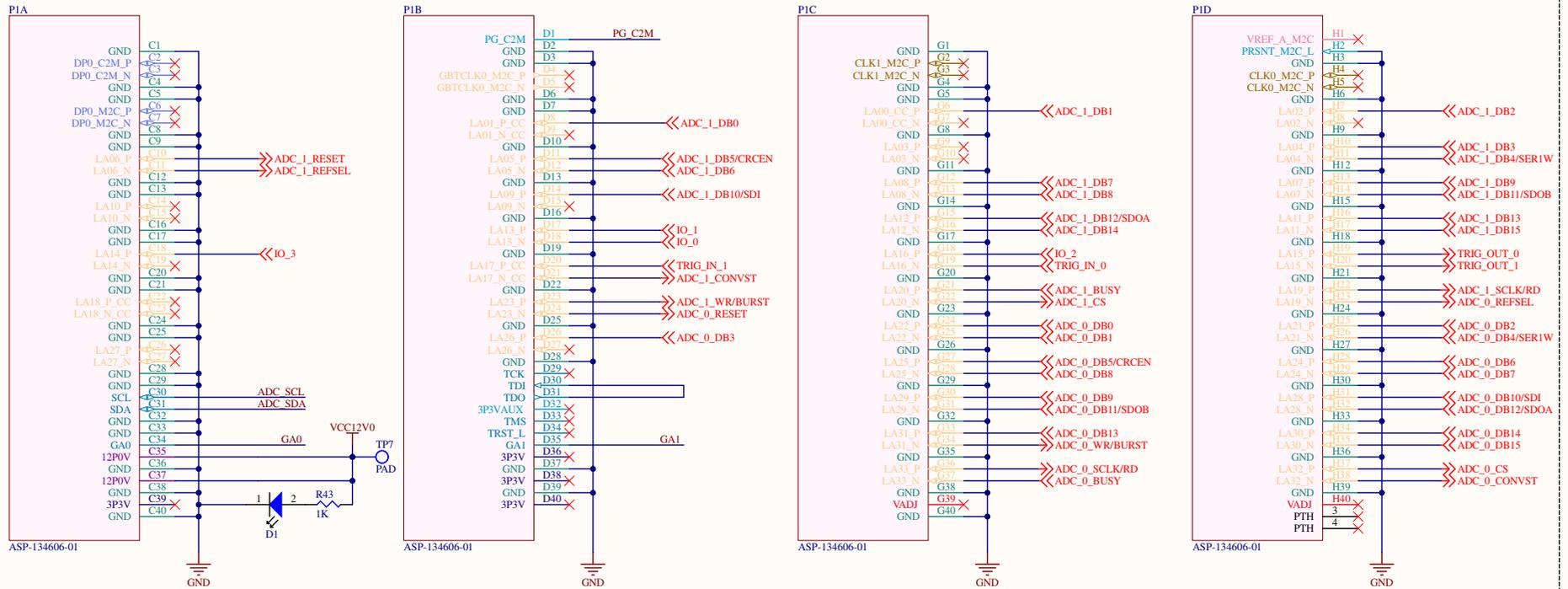
VCC5V0 POWER



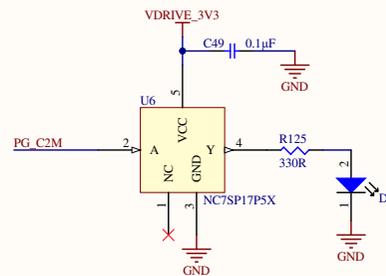
VREF_2V5 POWER



FMC LPC



PG_C2M



ADC EEPROM

